NSN 5962-00-752-9691

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Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Maximum Power Dissipation Rating:
110.0 milliwatts
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and edge triggered and presettable and w/clear
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
9 input
Design Function And Quantity:
1 flip-flop, j-k, and input master slave and 1 flip-flop, j-k, clocked
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
39.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level
output
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:

Fiig:

A458a0

Yes - demil/mli