## NSN 5962-00-759-8970

Digital Microcircuit - Page 1 of 1

Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:

**Design Function And Quantity:** 

**Time Rating Per Chacteristic:** 

**Test Data Document:** 

**Case Outline Source And Designator:** 

-0-004-aa joint electron device engineering council

Quad 2 input

4 gate, nand

output

Shelf Life:

**Unit Of Measure:** 

**Demilitarization:** Yes - demil/mli

N/a

Fiig: A458a0



View Online at https://aerobasegroup.com/nsn/5962-00-759-8970 **Body Length:** Between 0.337 inches and 0.350 inches **Body Width:** Between 0.200 inches and 0.300 inches **Body Height:** Between 0.008 inches and 0.100 inches **Maximum Power Dissipation Rating:** 160.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and positive outputs and w/totem pole output **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Flat pack

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).