NSN 5962-00-874-5758

Digital Microcircuit - Page 1 of 2



View Online at https://aerobasegroup.com/nsn/5962-00-874-5758

о.	- d		nath:	
nı	mw	1 6		-

Between 0.660 inches and 0.785 inches

Body Width:

Between 0.220 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.180 inches

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and w/decoded output and w/enable and presettable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Design Function And Quantity:

2 flip-flop, clocked and 2 flip-flop, d-type

Case Outline Source And Designator:

T0-116 joint electron device engineering council

Terminal Surface Treatment:

Gold

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output

Precious Material And Location:

Terminal surface gold

Test Data Document:

17773-11308381 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

--

Demilitarization:

Yes - demil/mli

NSN 5962-00-874-5758

Digital Microcircuit - Page 2 of 2



Fiig:

A458a0