# NSN 5962-00-919-0530

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## **Body Length:**

Between 0.240 inches and 0.290 inches

### **Body Width:**

Between 0.120 inches and 0.150 inches

## **Body Height:**

Between 0.030 inches and 0.070 inches

# **Maximum Power Dissipation Rating:**

8.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and low power and monolithic and positive outputs

#### Inclosure Material:

Glass and metal

# Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Resistor-capacitor-transistor logic

#### Input Circuit Pattern:

Dual 3 input

#### **Design Function And Quantity:**

2 gate, nand-nor

# Case Outline Source And Designator:

T0-89 joint electron device engineering council

#### **Terminal Surface Treatment:**

Gold

#### Voltage Rating And Type Per Characteristic:

8.0 volts power source

#### Time Rating Per Chacteristic:

60.00 nanoseconds propagation delay time, low to high level output

#### **Special Features:**

Formed leads; w/insulators

#### **Precious Material And Location:**

Body gold and terminals gold

## **Precious Material:**

Gold

# Terminal Type And Quantity:

10 pin

# Shelf Life:

N/a

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Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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