# NSN 5962-00-936-9382

Digital Microcircuit - Page 1 of 2



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### **Body Length:**

Between 0.240 inches and 0.260 inches

### Body Width:

Between 0.120 inches and 0.150 inches

### **Body Height:**

Between 0.030 inches and 0.070 inches

### **Maximum Power Dissipation Rating:**

3.8 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Monolithic and hermetically sealed and asynchronous and synchronous and w/enable and w/clear and presettable and low power

#### **Inclosure Material:**

Glass and metal

## Inclosure Configuration:

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

9 input

#### **Design Function And Quantity:**

1 flip-flop, clocked and 1 flip-flop, j-k, and input master slave

#### Case Outline Source And Designator:

T0-84 joint electron device engineering council

#### **Terminal Surface Treatment:**

Gold

#### Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

#### Precious Material And Location:

Terminal surface gold

#### **Precious Material:**

Gold

# Test Data Document:

06481-970081 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 flat leads

## Shelf Life:

N/a

## Unit Of Measure:

# NSN 5962-00-936-9382

Digital Microcircuit - Page 2 of 2

# Demilitarization:

Yes - demil/mli

Fiig:

A458a0

