NSN 5962-00-982-1686

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Body Outside Diameter:

Between 0.335 inches and 0.370 inches

Body Height:

Between 0.165 inches and 0.185 inches

Maximum Power Dissipation Rating:

12.0 milliwatts

Operating Tempurature Range:

+0.0/+70.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and internally compensated

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Can

Output Logic Form:

Diode-transistor logic

Input Circuit Pattern:

Dual 2 input

Design Function And Quantity:

2 gate, nand-nor

Case Outline Source And Designator:

T0-100 joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

03538-7836559 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

10 pin

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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