NSN 5962-01-003-0366

Digital Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-003-0366

Overall Length:
0.785 inches
Overall Height:
0.325 inches
Overall Width:
0.310 inches
Body Length:
0.785 inches
Body Width:
0.280 inches
Body Height:
0.180 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Burn in and w/clear and w/clock
Inclosure Material:
Ceramic
Incleaure Configuration.
Inclosure Configuration:
Dual-in-line
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Dual-in-line
Dual-in-line Output Logic Form:
Dual-in-line Output Logic Form: Transistor-transistor logic
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity: 14 printed circuit
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity: 14 printed circuit Shelf Life:
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity: 14 printed circuit Shelf Life: N/a
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity: 14 printed circuit Shelf Life: N/a
Dual-in-line Output Logic Form: Transistor-transistor logic Input Circuit Pattern: 8 input Design Function And Quantity: 2 flip-flop, j-k, master slave Terminal Surface Treatment: Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source Terminal Type And Quantity: 14 printed circuit Shelf Life: N/a Unit Of Measure:

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