NSN 5962-01-003-0367

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and burn in and medium speed and monolithic
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, or
Current Rating Per Characteristic:
38.00 milliamperes reverse current, dc absolute
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
22.00 nanoseconds propagation delay
Test Data Document:
73030-3e8071/00 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
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Demilitarization:
Yes - demil/mli
Fiig:
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