NSN 5962-01-010-1432

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| Body Length: |
|--|
| Between 1.200 inches and 1.290 inches |
| Body Width: |
| Between 0.515 inches and 0.580 inches |
| Body Height: |
| Between 0.050 inches and 0.230 inches |
| Maximum Power Dissipation Rating: |
| 650.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 6 input |
| Design Function And Quantity: |
| 1 gate, exclusive or |
| Case Outline Source And Designator: |
| -0-015-aa joint electron device engineering council |
| Time Rating Per Chacteristic: |
| 36.00 nanoseconds propagation delay time, low to high level output and 36.00 nanoseconds propagation delay time, high to low level |
| output |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |