NSN 5962-01-010-6445

Digital Microcircuit - Page 1 of 1



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|---|
| Body Length: |
| Between 0.755 inches and 0.785 inches |
| Body Width: |
| Between 0.245 inches and 0.280 inches |
| Body Height: |
| 0.180 inches |
| Maximum Power Dissipation Rating: |
| 110.0 milliwatts |
| Operating Tempurature Range: |
| +0.0/+70.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| W/clear and w/clock and w/preset and hermetically seale |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 9 input |
| Design Function And Quantity: |
| 1 flip-flop, j-k |
| Current Rating Per Characteristic: |
| 26.00 milliamperes reverse current, dc absolute |
| Terminal Surface Treatment: |
| Solder |
| Product Name: |
| Edge-triggered j-k flip-flop |
| Voltage Rating And Type Per Characteristic: |
| -0.5 volts power source and 7.0 volts power source |
| Terminal Type And Quantity: |
| 14 pin |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |

Fiig:

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