# NSN 5962-01-011-1331

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.660 inches and 0.785 inches

## Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

## **Maximum Power Dissipation Rating:**

100.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and edge triggered and w/clear

#### **Inclosure Material:**

Ceramic and glass

## Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

9 input

#### Design Function And Quantity:

1 flip-flop, j-k, and input master slave and 1 flip-flop, j-k, clocked

#### Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Time Rating Per Chacteristic:**

30.00 nanoseconds propagation delay time, low to high level output and 25.00 nanoseconds propagation delay time, high to low level output

#### **Special Features:**

With data lockout

#### **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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