NSN 5962-01-011-9590

Digital Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-011-9590

| The common of th |
|--|
| Body Length: |
| Between 0.660 inches and 0.785 inches |
| Body Width: |
| Between 0.220 inches and 0.280 inches |
| Body Height: |
| Between 0.140 inches and 0.180 inches |
| Maximum Power Dissipation Rating: |
| 50.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Hermetically sealed and monolithic and positive outputs and low power and negative edge triggered and presettable and schottky and |
| w/clear |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Input Circuit Pattern: |
| 8 input |
| Design Function And Quantity: |
| 2 flip-flop, j-k, clocked |
| Case Outline Source And Designator: |
| T0-116 joint electron device engineering council |
| Terminal Surface Treatment: |
| Solder |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity: |
| 14 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| Demilitarization: |
| Yes - demil/mli |
| |

Fiig:

A458a0