# NSN 5962-01-013-2717

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

180.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Monolithic and hermetically sealed and positive outputs and w/enable and w/disable and bipolar and w/buffered output and expandable and

3-state output and w/totem pole output

**Inclosure Material:** 

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Quad 2 input

## **Design Function And Quantity:**

4 buffer, noninverting and 4 gate, and

## Case Outline Source And Designator:

T0-116 joint electron device engineering council

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Shelf Life:

N/a

#### Unit Of Measure:

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#### **Demilitarization:**

Yes - demil/mli

#### Fiig:

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