NSN 5962-01-015-0272

Memory Microcircuit - Page 1 of 1



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Body	Length:
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Between 0.370 inches and 0.395 inches

Body Width:

Between 0.240 inches and 0.260 inches

Body Height:

Between 0.050 inches and 0.085 inches

Maximum Power Dissipation Rating:

500.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and w/enable and expandable and w/buffered output and wire-or outputs and bipolar and programmable and programmed and w/decoded output

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

8 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

25.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Rom

Test Data Document:

82577-932085 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

24 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

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