## NSN 5962-01-015-8537

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## **Body Length:** 0.796 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** Between 0.140 inches and 0.185 inches **Maximum Power Dissipation Rating:** 216.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -55.0/+125.0 degrees celsius **Features Provided:** Hermetically sealed and monolithic and positive outputs and w/open collector **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** Quad 2 input **Design Function And Quantity:** 4 gate, nand buffer **Case Outline Source And Designator:** D-1 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: -1.5 volts power source and 5.5 volts power source **Test Data Document:** 81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.). **Terminal Type And Quantity:** 14 printed circuit **Specification Data:** 81349-mil-m-38510/8 government specification Shelf Life: N/a Unit Of Measure: **Demilitarization:** Yes - demil/mli

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