NSN 5962-01-016-1280

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Body	Length:
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Between 0.750 inches and 0.795 inches

Body Width:

Between 0.245 inches and 0.300 inches

Body Height:

Between 0.120 inches and 0.195 inches

Maximum Power Dissipation Rating:

400.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and 3-state output and complementary outputs and w/decoded output and w/disable and w/enable and w/strobe

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

12 input

Design Function And Quantity:

1 multiplexer, eight to one line

Case Outline Source And Designator:

-0-001-ag joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

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