NSN 5962-01-017-3504

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 0.750 inches and 0.770 inches
Body Width:
Between 0.260 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.160 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed and positive outputs and bipolar and programmable and programmed and 3-state output and w/enable
and w/disable and w/buffered output and w/decoded output and expandable and wire-or outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
14 stage
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
35.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:

Fiig:

A458a0

Yes - demil/mli