NSN 5962-01-021-5878

Digital Microcircuit - Page 1 of 1



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| Body Length: |
|---|
| Between 0.750 inches and 0.795 inches |
| Body Width: |
| Between 0.245 inches and 0.300 inches |
| Body Height: |
| Between 0.120 inches and 0.195 inches |
| Maximum Power Dissipation Rating: |
| 380.0 milliwatts |
| Operating Tempurature Range: |
| -30.0/+85.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+175.0 degrees celsius |
| Features Provided: |
| Monolithic and hermetically sealed and negative outputs and high speed and schottky and w/totem pole output |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Emitter-coupled logic |
| Input Circuit Pattern: |
| Quad 2 input |
| Design Function And Quantity: |
| 4 translator, level |
| Case Outline Source And Designator: |
| -0-001-ag joint electron device engineering council |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 0.0 volts power source |
| Test Data Document: |
| $96906\text{-mil-std-883} \ standard \ (includes \ industry \ or \ association \ standards, \ individual \ manufactureer \ standards, \ etc.).$ |
| Terminal Type And Quantity: |
| 16 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| - |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |

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