NSN 5962-01-021-7602

Digital Microcircuit - Page 1 of 2



View Online at https://aerobasegroup.com/nsn/5962-01-021-7602

view Offiline at https://defobasegroup.com/histi/3302-01-021-7002
Body Length:
Between 0.240 inches and 0.275 inches
Body Width:
Between 0.160 inches and 0.185 inches
Body Height:
Between 0.030 inches and 0.070 inches
Maximum Power Dissipation Rating:
60.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
3 wide 3 input
Design Function And Quantity:
1 gate, and-or invert
Case Outline Source And Designator:
T0-85 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output
Test Data Document:
27963-57533100 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:

Yes - demil/mli

NSN 5962-01-021-7602Digital Microcircuit - Page 2 of 2



Fiig:

A458a0