## NSN 5962-01-021-8058

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| view Offilite at https://defobasegroup.com/hsr//0902-01-021-0000  |
|---|
| Body Length:  |
| Between 0.371 inches and 0.409 inches   |
| Body Width:   |
| Between 0.247 inches and 0.283 inches   |
| Body Height:  |
| Between 0.050 inches and 0.075 inches   |
| Operating Tempurature Range:  |
| +0.0/+75.0 degrees celsius  |
| Storage Tempurature Range:  |
| -65.0/+150.0 degrees celsius  |
| Features Provided:  |
| Monolithic and hermetically sealed and high speed and complementary outputs and positive outputs                                    |
| Inclosure Material:   |
| Ceramic and glass   |
| Inclosure Configuration:  |
| Flat pack   |
| Output Logic Form:  |
| Transistor-transistor logic   |
| Input Circuit Pattern:  |
| Dual 4 input  |
| Design Function And Quantity:   |
| 2 multiplexer   |
| Terminal Surface Treatment:   |
| Solder  |
| Voltage Rating And Type Per Characteristic:   |
| 5.5 volts power source  |
| Time Rating Per Chacteristic:   |
| 36.00 nanoseconds propagation delay time, low to high level output and 36.00 nanoseconds propagation delay time, high to low level  |
| output  |
| Test Data Document:   |
| 05869-717360-30 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing |
| etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)                   |
| Terminal Type And Quantity:   |
| 16 flat leads   |
| Shelf Life:   |
| N/a   |
| Unit Of Measure:  |
|   |
| Demilitarization:   |
| Yes - demil/mli   |

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