NSN 5962-01-023-0516

Memory Microcircuit - Page 1 of 1



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Features Provided:
Monolithic and burn in and w/resistor
Output Logic Form:
Metal oxide-semiconductor logic
nput Circuit Pattern:
24 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Memory Device Type:
Rom
Test Data Document:
37990-755090a0071 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Shelf Life:
N/a
Unit Of Measure:
-
Demilitarization:
Yes - demil/mli
Filg:
A458a0