NSN 5962-01-024-4406

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 1.100 inches and 1.250 inches

Body Width:

Between 0.580 inches and 0.610 inches

Body Height:

Between 0.080 inches and 0.120 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and programmed and w/disable and w/enable

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

8 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Rom

Test Data Document:

30003-247as-c2264 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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