NSN 5962-01-024-9330

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Body Length:
Between 0.660 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.280 inches
Body Height:
0.185 inches
Maximum Power Dissipation Rating:
110.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+200.0 degrees celsius
Features Provided:
Asynchronous and negative edge triggered and hermetically sealed and synchronous
Inclosure Material:
Ceramic or glass or metal
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 3 input
Design Function And Quantity:
2 flip-flop, j-k, clocked
Case Outline Source And Designator:
T0-116 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
15.00 nanoseconds propagation delay time, low to high level output
Test Data Document:
30003-247as-c0326 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization: Yes - demil/mli

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