## NSN 5962-01-025-2589

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## **Body Length:**

Between 0.740 inches and 0.810 inches

## Body Width:

Between 0.280 inches and 0.300 inches

## **Body Height:**

Between 0.110 inches and 0.140 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

Dc-sr segment tipi fscm 14155

## Features Provided:

Bipolar and hermetically sealed and monolithic and programmable and w/enable and w/open collector and 3-state output and programmed

#### **Inclosure Material:**

## Ceramic and metal

#### **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

6 input

### **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-1.0 volts power source and 5.5 volts power source

## Time Rating Per Chacteristic:

30.00 nanoseconds propagation delay time, low to high level output

## Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

## Test Data Document:

94990-51-+01424e drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

16 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

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