# NSN 5962-01-025-2596

Digital Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.755 inches and 0.785 inches

### **Body Width:**

0.280 inches

# **Body Height:**

Between 0.145 inches and 0.175 inches

# **Maximum Power Dissipation Rating:**

325.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### End Application:

Test set adapter

#### Features Provided:

Hermetically sealed and high speed and schottky and w/disable and 3-state output and monolithic

# Inclosure Material:

Ceramic

### Inclosure Configuration:

Dual-in-line

# Output Logic Form:

Transistor-transistor logic

# Input Circuit Pattern:

8 input

### **Design Function And Quantity:**

8 buffer, inverting

# **Terminal Surface Treatment:**

Solder

### Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

### **Terminal Type And Quantity:**

16 printed circuit

Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

No

Fiig:

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