# NSN 5962-01-026-2463

Digital Microcircuit - Page 1 of 2



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# **Body Length:**

Between 0.337 inches and 0.350 inches

# Body Width:

Between 0.235 inches and 0.265 inches

# **Body Height:**

Between 0.060 inches and 0.080 inches

# **Maximum Power Dissipation Rating:**

10.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **Features Provided:**

Hermetically sealed and burn in and monolithic and positive outputs

#### Inclosure Material:

Ceramic

## Inclosure Configuration:

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

Quad 2 input

## **Design Function And Quantity:**

4 gate, nand

## Case Outline Source And Designator:

Mo-004aa joint electron device engineering council

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

7.0 volts power source

## **Time Rating Per Chacteristic:**

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level

output

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## Terminal Type And Quantity:

14 pin

# Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

# NSN 5962-01-026-2463

Digital Microcircuit - Page 2 of 2

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