# NSN 5962-01-027-5652

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## Body Length:

0.808 inches

Body Width:

0.294 inches

### **Body Height:**

Between 0.154 inches and 0.179 inches

#### Maximum Power Dissipation Rating:

1.0 watts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

An/sps-48 (v) fcadt fscm 24930

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and bipolar and programmable and programmed and w/buffered output and w/enable

#### Inclosure Material:

Ceramic and glass

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

6 input

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

1.5 volts power source and 5.5 volts power source

#### **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output

#### Memory Device Type:

Rom

#### Memory Capacity:

Unknown

#### Test Data Document:

24930-g390452 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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### Demilitarization:

Yes - demil/mli

Fiig:

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