NSN 5962-01-029-4508

Digital Microcircuit - Page 1 of 1

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| Body Length: |
|--|
| Between 0.240 inches and 0.265 inches |
| Body Width: |
| Between 0.240 inches and 0.265 inches |
| Body Height: |
| Between 0.030 inches and 0.080 inches |
| Maximum Power Dissipation Rating: |
| 200.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| End Application: |
| Ma-1/asq-25 f106a/b |
| Features Provided: |
| Hermetically sealed and monolithic and positive outputs |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Flat pack |
| Output Logic Form: |
| Diode-transistor logic |
| Input Circuit Pattern: |
| Triple 3 input |
| Design Function And Quantity: |
| 3 gate, nand-nor |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| -1.5 volts power source and 5.5 volts power source |
| Test Data Document: |
| 30221-44a340316 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, |
| etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) |
| Terminal Type And Quantity: |
| 14 flat leads |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |