## NSN 5962-01-032-6269

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Design Function And Quantity:
2 flip-flop, j-k, master slave
Case Outline Source And Designator:
T0-84 joint electron device engineering council
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:

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