NSN 5962-01-032-6270

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Operating Tempurature Range: -55.0/+125.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Design Function And Quantity:

1 flip-flop, j-k

Case Outline Source And Designator:

To-84 joint electron device engineering council

Precious Material And Location:

Terminal surface gold

Precious Material:

Gold

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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