# NSN 5962-01-033-2274

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

50.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

Cnt mes set an/tlq-17a fscm 72314

#### **Features Provided:**

Hermetically sealed and monolithic and schottky and negative edge triggered and w/clear and resettable and positive outputs and low power

## **Inclosure Material:**

Ceramic and glass

#### **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Dual 4 input

# **Design Function And Quantity:**

2 flip-flop, j-k, clocked and 2 flip-flop, j-k, master slave

# Case Outline Source And Designator:

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

# Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

# **Time Rating Per Chacteristic:**

20.00 nanoseconds propagation delay time, low to high level output

# Test Data Document:

81349-mil-std-883 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

# Terminal Type And Quantity:

14 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

Fiig:

A458a0

