# NSN 5962-01-035-5405

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Between 0.750 inches and 0.795 inches

**Body Width:** 

Between 0.245 inches and 0.300 inches

**Body Height:** 

Between 0.120 inches and 0.195 inches

**Maximum Power Dissipation Rating:** 

625.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

4920-00-507-1587

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and bipolar and w/buffered output and w/decoded output and expandable and wire-or outputs and w/disable

#### **Inclosure Material:**

Ceramic and glass

#### **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### **Input Circuit Pattern:**

6 input

# **Case Outline Source And Designator:**

-0-001-ag joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

# **Time Rating Per Chacteristic:**

50.00 nanoseconds propagation delay time, low to high level output and 50.00 nanoseconds propagation delay time, high to low level output

#### **Memory Device Type:**

Rom

# **Memory Capacity:**

Unknown

# **Test Data Document:**

14210-601009 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

16 printed circuit

## Shelf Life:

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**Unit Of Measure:** 

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Demilitarization:

Yes - demil/mli

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