NSN 5962-01-039-3428

Memory Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-039-3428
Body Length:
Between 1.240 inches and 1.280 inches
Body Width:
Between 0.510 inches and 0.550 inches
Body Height:
Between 0.120 inches and 0.150 inches
Maximum Power Dissipation Rating:
625.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and w/enable and programmable and programmed and expandable and w/buffered
output and wire-or outputs and w/decoded output and w/disable
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
75.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Rom
Test Data Document:
36452-116598 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Fiig:

Demilitarization: Yes - demil/mli