# NSN 5962-01-040-8825

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# **Body Length:**

Between 1.140 inches and 1.290 inches

# **Body Width:**

Between 0.500 inches and 0.610 inches

# **Body Height:**

Between 0.120 inches and 0.185 inches

# **Maximum Power Dissipation Rating:**

580.0 milliwatts

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **End Application:**

Receiving set, countermeasures type an/wlr- (v) 2

## **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/enable and negative edge triggered

## **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

# **Output Logic Form:**

Transistor-transistor logic

# Input Circuit Pattern:

Dual 6 input

## **Design Function And Quantity:**

2 register, buffer

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

## **Terminal Type And Quantity:**

24 pin

#### Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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