NSN 5962-01-041-3495

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Fiig: A458a0



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Body Length:
1.290 inches
Body Width:
Between 0.500 inches and 0.610 inches
Body Height:
0.210 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 5 input
Design Function And Quantity:
2 latch, bistable
Case Outline Source And Designator:
D-3 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
65.00 nanoseconds propagation delay time, low to high level output and 65.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
80063-sm-a-699715 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli