# NSN 5962-01-043-9133

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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

0.180 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### **Features Provided:**

Schottky and hermetically sealed

#### **Inclosure Material:**

Silicon

#### Inclosure Configuration:

Flat pack

## Output Logic Form:

#### Transistor-transistor logic

Input Circuit Pattern:

2 input

### **Design Function And Quantity:**

2 gate, nand

# Voltage Rating And Type Per Characteristic:

5.5 volts input

## **Special Features:**

Selected item drawing

### **Test Data Document:**

98230-0n196150 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# Terminal Type And Quantity:

14 pin

## Shelf Life:

N/a

### Unit Of Measure:

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#### **Demilitarization:**

Yes - demil/mli

#### Fiig:

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