NSN 5962-01-043-9838

Memory Microcircuit - Page 1 of 1



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|--|
| Body Length: |
| Between 1.175 inches and 1.300 inches |
| Body Width: |
| Between 0.490 inches and 0.550 inches |
| Body Height: |
| 0.150 inches |
| Maximum Power Dissipation Rating: |
| 2.0 watts |
| Operating Tempurature Range: |
| +0.0/+70.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+125.0 degrees celsius |
| Features Provided: |
| Burn in, mil-std-883, class b and edge triggered and erasable and hermetically sealed and monolithic and positive outputs and w/buffered |
| output and w/decoded output |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line Dual-in-line |
| Output Logic Form: |
| Metal oxide-semiconductor logic |
| Input Circuit Pattern: |
| 12 input |
| Case Outline Source And Designator: |
| D-1 mil-m-38510 |
| Terminal Surface Treatment: |
| Solder |
| Voltage Rating And Type Per Characteristic: |
| 0.5 volts power source |
| Time Rating Per Chacteristic: |
| 650.00 nanoseconds propagation delay time, low to high level output and 650.00 nanoseconds propagation delay time, high to low level |
| output |
| Memory Device Type: |
| Rom |
| Terminal Type And Quantity: |
| 24 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |

Fiig: A458a0

Yes - demil/mli