NSN 5962-01-044-4604

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Between 0.337 inches and 0.350 inches

Body Width:

Between 0.200 inches and 0.300 inches

Body Height:

Between 0.008 inches and 0.100 inches

Maximum Power Dissipation Rating:

200.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and dual edge triggered and negative edge triggered and w/enable and w/clear and high speed

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

8 input

Design Function And Quantity:

2 flip-flop, j-k, clocked

Case Outline Source And Designator:

-0-004-aa joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

15.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

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