NSN 5962-01-045-5266

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| Inclosure Material: |
|-----------------------------|
| Ceramic |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Transistor-transistor logic |
| Terminal Surface Treatment: |
| Solder |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| No |
| Fiig: |

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