NSN 5962-01-046-4790

Memory Microcircuit - Page 1 of 1



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Body	Le	ngth:
Betwe	en	0.750

Between 0.750 inches and 0.770 inches

Body Width:

Between 0.260 inches and 0.280 inches

Body Height:

Between 0.140 inches and 0.160 inches

Operating Tempurature Range:

+0.0/+75.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and programmable and programmed and bipolar and w/decoded output and expandable and wire-or outputs and 3-state output and w/buffered output and high speed and w/disable and w/enable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-1.0 volts power source and 5.5 volts pac identification output

Time Rating Per Chacteristic:

70.00 nanoseconds propagation delay time, low to high level output and 70.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Memory Capacity:

Unknown

Test Data Document:

97384-56602-40003 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

16 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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