## NSN 5962-01-048-5210

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| Body Length:  |
|---|
| Between 0.740 inches and 0.870 inches   |
| Body Width:   |
| Between 0.275 inches and 0.310 inches   |
| Body Height:  |
| 0.180 inches  |
| Operating Tempurature Range:  |
| -55.0/+125.0 degrees celsius  |
| Storage Tempurature Range:  |
| -65.0/+150.0 degrees celsius  |
| Features Provided:  |
| Schottky and high speed and edge triggered  |
| Inclosure Material:   |
| Ceramic   |
| Inclosure Configuration:  |
| Dual-in-line  |
| Output Logic Form:  |
| Transistor-transistor logic   |
| Input Circuit Pattern:  |
| 10 input  |
| Design Function And Quantity:   |
| 1 register  |
| Time Rating Per Chacteristic:   |
| 12.00 nanoseconds propagation delay time, low to high level output and 17.00 nanoseconds propagation delay time, high to low leve |
| output  |
| Test Data Document:   |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).                |
| Shelf Life:   |
| N/a   |
| Unit Of Measure:  |
| <del></del>   |
| Demilitarization:   |
| Yes - demil/mli   |
| Fiig:   |
| A458a0  |
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