NSN 5962-01-050-4724

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Body Length:
Between 0.755 inches and 0.785 inches
Body Width:
Between 0.245 inches and 0.280 inches
Body Height:
Between 0.140 inches and 0.180 inches
Maximum Power Dissipation Rating:
270.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and synchronous and programmable and medium speed and edge triggered and
w/enable and w/clear and w/decoded output and w/strobe and gated output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
10 input
Design Function And Quantity:
1 multiplier, rate
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
26.00 nanoseconds propagation delay time, low to high level output and 35.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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