## NSN 5962-01-050-4725

Memory Microcircuit - Page 1 of 2



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Body Length:
Between 1.230 inches and 1.280 inches
Body Width:
Between 0.520 inches and 0.560 inches
Body Height:
Between 0.160 inches and 0.200 inches
Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
+0.0/+75.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and negative outputs and positive outputs and w/enable and expandable and w/buffered output and
wire-or outputs and programmable and programmed and w/decoded output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 75.00 nanoseconds propagation delay time, low to high level
output
Memory Device Type:
Prom
Memory Capacity:
Unknown
Test Data Document:
08783-37651978 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 printed circuit

Demilitarization:

**Unit Of Measure:** 

Shelf Life:

N/a

## NSN 5962-01-050-4725

Memory Microcircuit - Page 2 of 2



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