NSN 5962-01-050-4726

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Body Length:
Between 0.750 inches and 0.855 inches
Body Width:
Between 0.240 inches and 0.300 inches
Body Height:
Between 0.150 inches and 0.185 inches
Maximum Power Dissipation Rating:
650.0 milliwatts
Operating Tempurature Range:
+0.0/+75.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and monolithic and positive outputs and programmable and programmed and w/enable and w/disable and w/open
collector and w/storage and w/decoded output and high speed and w/buffered output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
6 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Prom
Memory Capacity:
Unknown
Test Data Document:
08783-37654869 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 printed circuit

Demilitarization:

Unit Of Measure:

Shelf Life:

N/a

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