# NSN 5962-01-050-6927

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D - J.	 nath:

Between 0.750 inches and 0.795 inches

**Body Width:** 

Between 0.245 inches and 0.300 inches

**Body Height:** 

Between 0.120 inches and 0.195 inches

**Maximum Power Dissipation Rating:** 

657.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and w/enable and high speed and programmable and programmed and w/open collector and schottky and bipolar and w/buffered output and w/decoded output and expandable and w/disable and positive outputs

#### **Inclosure Material:**

Ceramic and glass

# **Inclosure Configuration:**

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

#### **Input Circuit Pattern:**

10 input

#### **Case Outline Source And Designator:**

-0-001-ag joint electron device engineering council

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Time Rating Per Chacteristic:**

75.00 nanoseconds propagation delay time, low to high level output

# **Memory Device Type:**

Prom

## **Memory Capacity:**

Unknown

#### **Test Data Document:**

94117-7013655 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

# **Terminal Type And Quantity:**

16 printed circuit

# Shelf Life:

N/a

#### **Unit Of Measure:**

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Demilitarization:

Yes - demil/mli

Fiig:

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