## NSN 5962-01-051-1167

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Between 0.240 inches and 0.275 inches

**Body Width:** 

Between 0.160 inches and 0.185 inches

**Body Height:** 

Between 0.030 inches and 0.070 inches

**Maximum Power Dissipation Rating:** 

110.0 milliwatts

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **End Application:**

Communication system, type an/tsc-1 (v)

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/enable and negative edge triggered and w/complement and resettable and w/storage

#### **Inclosure Material:**

Ceramic and glass

## **Inclosure Configuration:**

Flat pack

## **Output Logic Form:**

Transistor-transistor logic

## **Input Circuit Pattern:**

Dual 4 input

## **Design Function And Quantity:**

2 flip-flop, j-k, clocked

## **Case Outline Source And Designator:**

T0-85 joint electron device engineering council

## Voltage Rating And Type Per Characteristic:

5.5 volts power source

## **Time Rating Per Chacteristic:**

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

## **Test Data Document:**

13499-351-7296 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

14 flat leads

## Shelf Life:

N/a

## **Unit Of Measure:**

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## Demilitarization:

Vec - demil/mli

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