## NSN 5962-01-051-3400

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Between 1.190 inches and 1.230 inches

**Body Width:** 

Between 0.580 inches and 0.610 inches

**Body Height:** 

Between 0.080 inches and 0.120 inches

**Maximum Power Dissipation Rating:** 

675.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### **Storage Tempurature Range:**

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and schottky and high speed and w/enable and w/storage and w/strobe and w/disable and 3-state output and programmable and programmed and w/decoded output

#### **Inclosure Material:**

Ceramic and metal

#### **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## **Input Circuit Pattern:**

12 input

#### **Terminal Surface Treatment:**

Gold

## **Voltage Rating And Type Per Characteristic:**

5.5 volts power source

#### **Time Rating Per Chacteristic:**

35.00 nanoseconds propagation delay time, low to high level output and 45.00 nanoseconds propagation delay time, high to low level output

#### **Memory Device Type:**

Rom

## **Memory Capacity:**

Unknown

#### **Precious Material And Location:**

Terminal surfaces gold

#### **Precious Material:**

Gold

## **Test Data Document:**

08783-37654883 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## **Terminal Type And Quantity:**

16 printed circuit

#### Shelf Life:

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Demilitarization:

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