NSN 5962-01-053-6325

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Body Length:

Between 0.337 inches and 0.350 inches

Body Width:

Between 0.200 inches and 0.300 inches

Body Height:

Between 0.008 inches and 0.100 inches

Maximum Power Dissipation Rating:

51.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and hermetically sealed and positive outputs and w/totem pole output and high speed and schottky

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 5 input

Design Function And Quantity:

2 gate, nor

Case Outline Source And Designator:

-0-004-aa joint electron device engineering council

Time Rating Per Chacteristic:

10.00 nanoseconds propagation delay time, low to high level output and 10.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

82577-932624 standard (includes industry or association standards, individual manufactureer standards, etc.).

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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