NSN 5962-01-054-8027

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Bodv	l er	nath:
Douv	LCI	ıuıı.

Between 0.240 inches and 0.275 inches

Body Width:

Between 0.240 inches and 0.260 inches

Body Height:

Between 0.030 inches and 0.070 inches

Maximum Power Dissipation Rating:

150.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Hermetically sealed and monolithic and positive outputs and high speed and presettable and w/enable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Design Function And Quantity:

1 flip-flop, j-k, and/or input and 1 flip-flop, j-k, clocked and 1 flip-flop, j-k, master slave

Case Outline Source And Designator:

T0-86 joint electron device engineering council

Terminal Surface Treatment:

Gold

Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

Time Rating Per Chacteristic:

21.00 nanoseconds propagation delay time, low to high level output and 27.00 nanoseconds propagation delay time, high to low level output

Precious Material And Location:

Terminals gold

Precious Material:

Gold

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

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Demilitarization:

Yes - demil/mli

Fiig:

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