NSN 5962-01-056-8060

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Body Length:

Between 0.750 inches and 0.795 inches

Body Width:

Between 0.245 inches and 0.300 inches

Body Height:

Between 0.120 inches and 0.195 inches

Maximum Power Dissipation Rating:

237.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Data reduction central type an/gyq-1 (v)

Features Provided:

Hermetically sealed and monolithic and positive outputs and low power and w/open collector

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

7 input

Design Function And Quantity:

1 decoder, binary coded decimal to seven segment

Case Outline Source And Designator:

-0-001-ag joint electron device engineering council

Time Rating Per Chacteristic:

85.00 nanoseconds propagation delay time, low to high level output and 85.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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